

Figure 1: Top Level Block Diagram

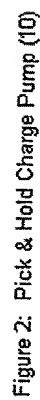
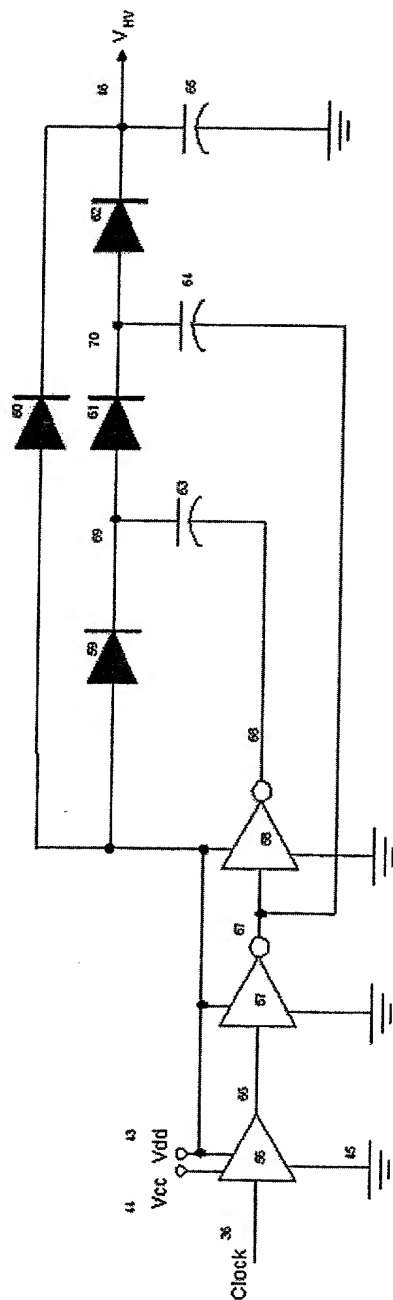


Figure 3: Generator Start/Stop Interface (9)



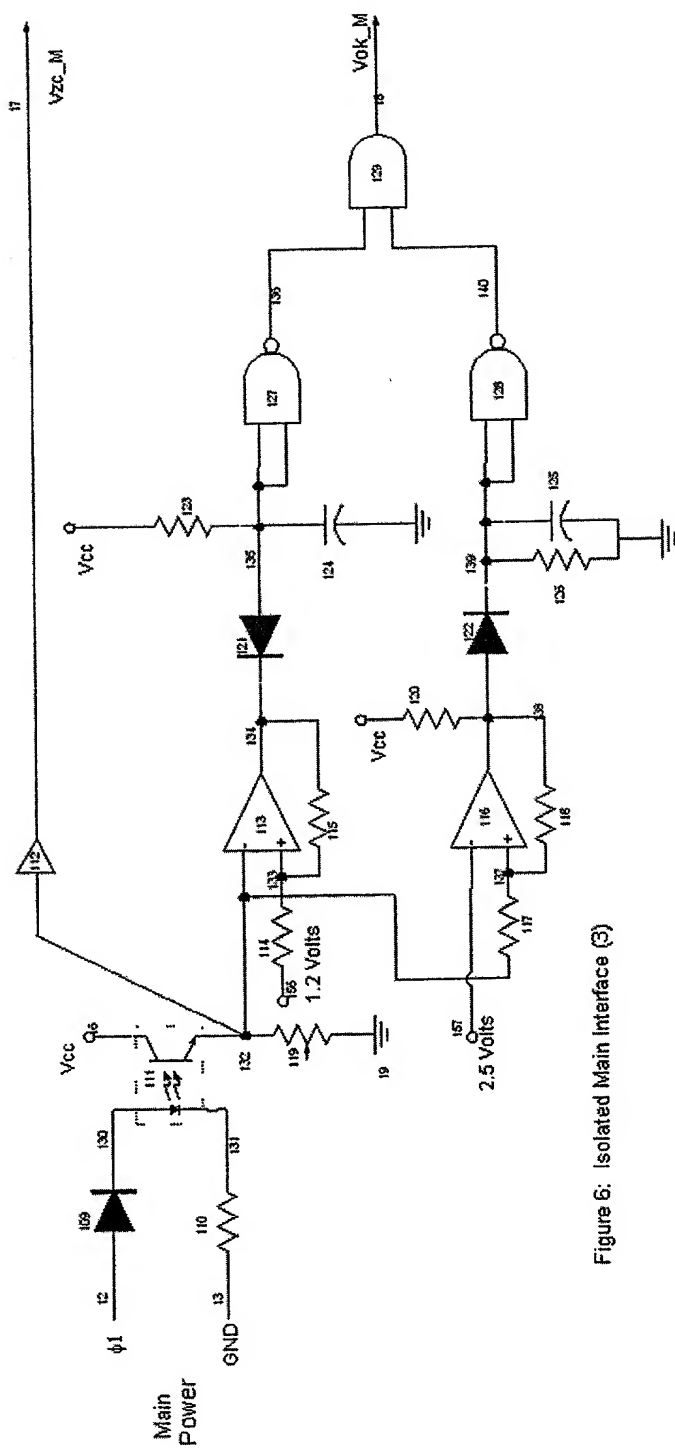


Figure 6: Isolated Main Interface (3)

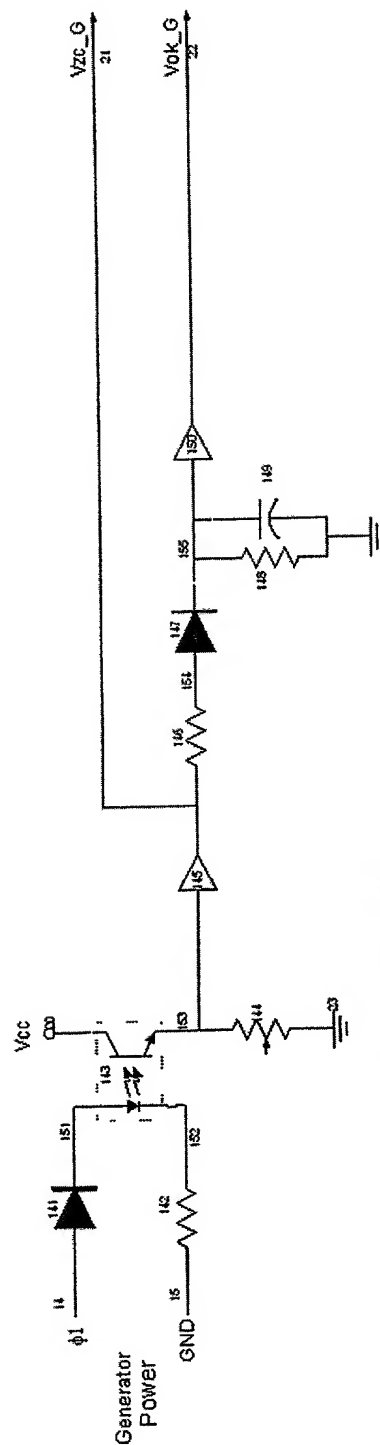


Figure 7: Isolated Generator Interface (4)